

**What is Claimed is:**

1. An electrical circuit, comprising:
  - functional circuitry for performing normal operating functions of the electrical circuit;
  - an output buffer coupled to said functional circuitry for receiving output signals from said functional circuitry and driving said output signals out of the electrical circuit to an external destination;
  - a test data path for providing test data;
  - memory circuitry coupled to said test data path for storing test data received from said test data path; and
  - a latching circuit connected to said memory circuitry for receiving and selectively latching test data stored in said memory circuitry, said latching circuit including said output buffer.